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TR2013-121 September 2013

Abstract

On-state gate leakage current behavior of AlGaN/GaN high electron mobility transistors (HEMTs) has been studied by using Technology Computer Aided Design (TCAD) simulation. We found the gate leakage current increases above the pinch-off voltage, which is different from the case of a two-terminal operation. This gate leakage current increase is due to self-heating effect at the gate edge of the drain side where the gate leakage occurs.

Topical Workshop on Heterostructure Microelectronics (TWHM)

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Simulation study of gate leakage current under three-terminal operation for AlGaN/GaN HEMTs

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Abstract

On-state gate leakage current behavior of AlGaN/GaN high electron mobility transistors (HEMTs) has been studied by using Technology Computer Aided Design (TCAD) simulation. We found the gate leakage current increases above the pinch-off voltage, which is different from the case of a two-terminal operation. This gate leakage current increase is due to self-heating effect at the gate edge of the drain side where the gate leakage occurs.

Introduction

HEMTs using GaN and related compounds are very promising devices for RF high-power amplifiers due to their superior material properties. Although to date, many high RF performances have been reported, there is still a lot of room for their full potential to be played out [1, 2]. One of the important issues is the gate leakage current reduction. The gate leakage mechanism has been widely studied and the origin of the gate leakage current is the electron tunneling through the Schottky gate barrier [3-5]. However, gate leakage current behavior for three-terminal operation under on-state condition is not clearly understood, because most papers deal with behavior for two-terminal operation under off-state condition where large drain current is absent in a channel. In this paper, we have studied the unique characteristics of the gate leakage current of AlGaN/GaN HEMTs under the on-state condition by using TCAD simulation. The gate leakage current increases above the pinch-off voltage, when the self-heating effect introduces to rise of lattice temperature at the gate edge of the drain side.

Structure for TCAD Simulation

Figure 1 shows the schematic of a cross sectional structure of AlGaN/GaN HEMT which is used in our TCAD simulation. The positive polarity charges are set at the interface between the AlGaN barrier and the GaN channel in order to produce the 2DEG channel. A thin surface barrier model with a donor thin layer at the AlGaN surface is adopted for simulation of the gate leakage current [3, 5]. The self-heating effect is taken into account in this study using Silvaco ATLAS as the simulator [6].

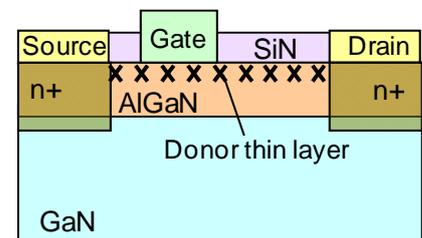


Fig. 1 Schematic cross sectional AlGaN/GaN HEMT structure.

TCAD Simulation results

We have simulated gate current (I_g) and drain current (I_d) as dependents of gate voltage (V_g) in order to study the impact of the self-heating effect. Figure 2 shows I_d increases monotonically as V_g increases whether with or without self-heating effect. On the other hand, I_g has very different characteristics between with and without self-heating effect. I_g decreases monotonically in the case of no self-heating, while I_g 's trend changes at about V_g of -3 V from decreasing to increasing in the case of the self-heating. The increase of I_g above V_g of -3 V cannot be observed by only using the gate leakage mechanism of a two-terminal operation. This should be considered as the effect of the increase of lattice temperature (Tl) due to the stronger self-heating generated by the larger I_d above the pinch-off voltage (-3.5 V).

In order to investigate the relationship between Tl and I_g , we simulated the I_g at various Tl without the self-heating. Figure 3 shows I_g and maximum Tl graphs at ambient temperature of 300 K. Figure 3 also shows I_g for the various Tl without self-heating effect. I_g with self-heating effect is almost the same as I_g without the self-heating, when maximum Tl is the same as Tl without self-heating effect (The case of Tl at 400 K is shown using the arrows in Fig. 3). Therefore, I_g increases with V_g at about V_g greater than -3 V is due to the increase of Tl as electron current in the channel increases.

Figure 4 shows Tl and electric field (E) profiles at $V_g = -2$ V where HEMT operates under the on-state condition. Tl has a maximum value near the gate edge of the drain side according to the product of I_d and E. From Fig. 5 (a) and

(b), the maximum value of TI exists near the peak value of E at the gate edge where the gate leakage arises. This is a reason why I_g with self-heating increases with V_g as opposed to decreasing I_g with increased V_g without the self-heating.

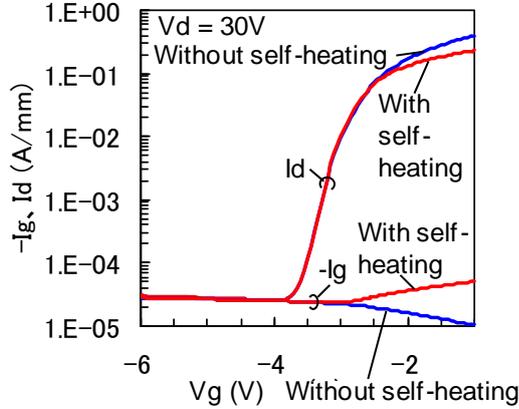


Fig. 2 Drain current and gate current depending on gate voltage at high drain voltage of 30 V with and without self-heating.

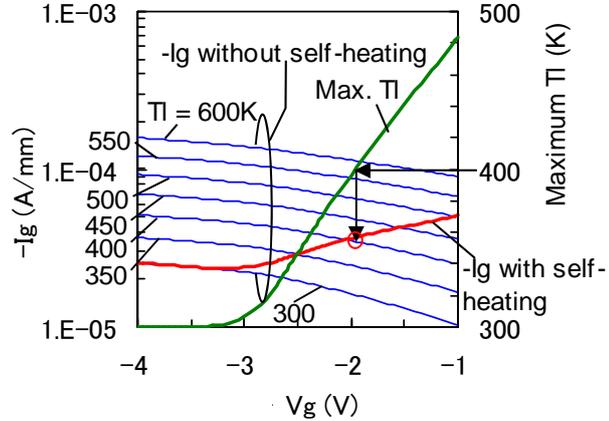


Fig. 3 Gate current for ambient temperature of 300K over-drown by current for various lattice temperatures. Maximum values of Lattice temperature for self-heating are also shown.

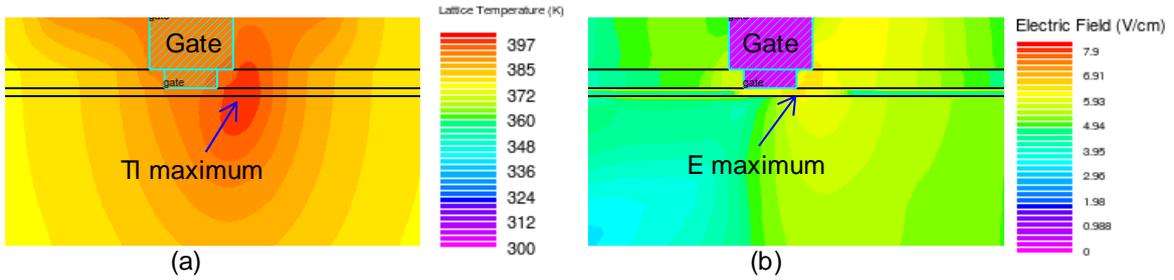


Fig. 4 (a) Lattice temperature and (b) electric field profiles at $V_g=-2V$ and $V_d=30V$ with self-heating effect.

Comparison with experimental data

We confirm the above described I_g behavior in a real AlGaIn/GaN HEMT device experiment. Figure 5 shows the comparison between model and measurement data of I_g and I_d as function of V_g . The model data have very good agreement with the measurement data for both I_d and I_g characteristics. Therefore, the impact on the gate leakage current in the on-state condition due to the increase in the lattice temperature induced by self-heating should be considered.

Summary

Self-heating at the on-state condition in AlGaIn/GaN HEMTs has strong impact on the gate leakage current behavior. Above the pinch-off voltage, I_g increases with V_g due to the self-heating of large I_d , whereas below pinch-off voltage, I_g follows the expected behavior, decreases as V_g increases.

References

- [1] U.Mishra, et al., Proc. IEEE, 96 (2008) 287.
- [2] K. Yamanaka, et al., IEEE MTT-S IMS Digest, 2012, TU4H4.
- [3] H. Hasegawa, et al., J. Vac. Sci. Technol. B 21, (2003) 1844.
- [4] J.Woo.Joh et al., IEEE Electron Devices, 58 (2011) 132.
- [5] K.Hayashi, et al., Jpn. J. Appl. Phys., to be published, 2013.
- [6] Atlas user manual, Ver.5.16.3.R. Silvaco, 2010.

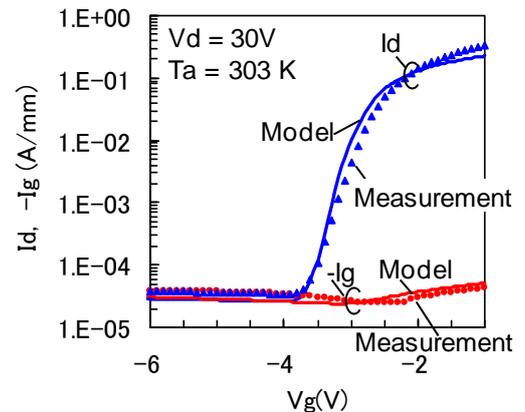


Fig. 5 Comparison between model and measurement for drain and gate current depending on gate voltage.